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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/360,472	07/23/1999	RETO STAMM	X-528-US	4229

24309 7590 04/11/2002

XILINX, INC
ATTN: LEGAL DEPARTMENT
2100 LOGIC DR
SAN JOSE, CA 95124

EXAMINER

PHAN, THAI Q

ART UNIT	PAPER NUMBER
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2123

DATE MAILED: 04/11/2002

Please find below and/or attached an Office communication concerning this application or proceeding.

PA

Office Action Summary

Application No.
09/360,472

Applicant(s)
Stamm et al.

Examiner
Thal Phan

Art Unit
2123

-- Th MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136 (a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).
- Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☐ Responsive to communication(s) filed on _____
- 2a) ☐ This action is FINAL. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 35 C.D. 11; 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-20 is/are pending in the application.
- 4a) Of the above, claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-20 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claims _____ are subject to restriction and/or election requirements.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are objected to by the Examiner.
- 11) ☐ The proposed drawing correction filed on _____ is: a) ☐ approved b) ☐ disapproved.
- 12) ☐ The oath or declaration is objected to by the Examiner.

Priority under 35 U.S.C. § 119

- 13) ☐ Acknowledgement is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d).
- a) ☐ All b) ☐ Some* c) ☐ None of:
- ☐ Certified copies of the priority documents have been received.
 - ☐ Certified copies of the priority documents have been received in Application No. _____.
 - ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- *See the attached detailed Office action for a list of the certified copies not received.
- 14) ☐ Acknowledgement is made of a claim for domestic priority under 35 U.S.C. § 119(e).

Attachment(s)

- 15) ☒ Notice of References Cited (PTO-892) 18) ☐ Interview Summary (PTO-413) Paper No(s). _____
- 16) ☒ Notice of Draftsperson's Patent Drawing Review (PTO-948) 19) ☐ Notice of Informal Patent Application (PTO-152)
- 17) ☐ Information Disclosure Statement(s) (PTO-1449) Paper No(s). _____ 20) ☐ Other:

DETAILED ACTION

This Office action is responsive to patent application S/N: 09/360,472. Claims 1-20 are pending in this official action.

Drawings

1. The drawings are objected by the Draftsperson (see enclosed Form PTO948).

Claim Rejections - 35 USC § 102

2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless --

(e) the invention was described in a patent granted on an application for patent by another filed in the United States before the invention thereof by the applicant for patent, or on an international application by another who has fulfilled the requirements of paragraphs (1), (2), and (4) of section 371© of this title before the invention thereof by the applicant for patent.

3. Claims 1-20 are rejected under 35 U.S.C. 102(e) as being anticipated by Dockser et al., patent no. 5,963,454.

As per claim 1, Dockser anticipated method and system for designing complex function cores in an integrated circuit by compilation and synthesis with feature representation very identical to the claimed invention (Abstract). According to Dockser, the design method includes steps of randomly generating a set of parameter values for the logic cores (Figs. 1B, 4, "Disclosure of the Invention", col. 4), generating a netlist file for circuit design, and simulating the circuit behavior (Fig. 1B, cols. 6-8) for design verification.

As per claim 2, Dockser anticipated parameter values are randomly generated and bounded.

As per claim 4, Dockser anticipated user interface for input data values.

As per claims 3, and 5-17, Dockser anticipated design parameter values are randomly generated for logic core design, and design values are alternatively changed to meet design criteria, including permuting parameter variables, modifying parameters, etc. (col. 8, line 56 to col. 9, line 32, for example).

As per claim 18, Dockser anticipated system for testing and verifying logic circuit design, including a controller configured to generate design parameter variables, a logic core generator for generating netlist of the design from the logic core and design parameter variables, a logic simulator for simulating the design as claimed (Figs. 1B, 4, col. 4, col. 6, lines 52-63, col. 8, lines 24-34, col. 8, line 56 to col. 9, line 32, for example).

As per claim 19, in addition to limitations as shown in claim 18, Dockser also anticipated graphic user interface for designer. Thus, claim 19 is rejected in like manner.

As per claim 20, Dockser anticipated system for designing complex function cores in an integrated circuit by compilation and synthesis with feature representation very identical to the claimed invention (Abstract). According to Dockser, the design system includes means for randomly generating a set of parameter values for the logic cores (Figs. 1B, 4, "Disclosure of the Invention", col. 4), a netlist file generator for generating circuit design connection file, and simulating the circuit behavior (Fig. 1B, cols. 6-8) for design verification.

Conclusion

4. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

1. Patent no. 5,910,897, issued to Dangelo et al., June 1999
2. Patent no. 5,910,898, issued to Johannsen, David, June 1999
3. Patent no. 6,292,925 B1, issued to Dellinger et al., Sept. 2001

5. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Thai Phan whose telephone number is (703) 305-3812.

Any inquiry of a general nature or relating to the status of this application should be directed to the Group receptionist whose telephone number is (703)305-3900.

Any response to this action should be mailed to:

Commissioner of Patents and Trademarks
Washington, D.C. 20231

or faxed to:

(703) 746-7239, (for formal communications intended for entry)

Or:

(703) 746-7240 (for informal or draft communications, please label "PROPOSED"

or "DRAFT")

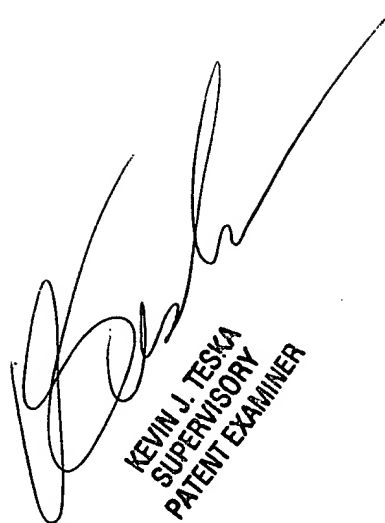
Hand-delivered responses should be brought to Crystal Park II, 2121 Crystal Drive,

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Arlington, VA., Sixth Floor (Receptionist).

April 4, 2002



KEVIN J. TESKA
SUPERVISORY
PATENT EXAMINER